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Schedule Update Analysis: Retained Logic versus Progress Override

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Abstract— Construction industry standard schedule specifications most often require retained logic as the schedule calculation setting when updating the schedule. Use of this calculation setting can sometimes prove problematic when out of sequence progress occurs and result in inaccurate information and artificially impacted float values.

The progress override calculation setting can also be problematic because using this method ignores predecessor schedule logic for activities in progress. Owners prefer retained logic and contractors prefer progress override. These differing viewpoints can impact the validity of the project schedule.

This paper will evaluate the pros and cons of using each calculation method, typical scheduling specification language, and give industry examples. The impact of using each method will be evaluated for proper schedule updates and delay claim analysis. Finally, recommendations will be offered for use of progress override when correcting out of sequence schedule logic is not possible.

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Introduction

Schedule development is a process where activities are identified and then network logic is applied before duration estimation occurs. Schedule logic identifies the dependency relationships between activities within a schedule and thus the work sequence to be followed while executing the project. There are four possible relationships between activity start and end and those of other activities¹.

- Finish-to-Start (FS): First activity must finish before second activity can start
- Start-to-Start (SS): Two activities start at the same time
- Finish-to-Finish (FF): Two activities must complete at the same time
- Start-to-Finish (SF): Activity must start before second activity can finish

Activities can be linked with Hard Logic or Soft Logic. Hard logic is utilized where activities must follow a prescribed sequence and is often dictated by physical restraints such as footings must be constructed before walls or columns. Soft logic represents preferential logic relationships that are not physically required but are preferred and often carried out in a different order upon execution.²

Sometimes soft logic linked activities begin without following predecessor schedule logic. When actual schedule progress occurs for activities that should not logically start based upon predecessor logic, this is termed “out-of-sequence progress”³. There could be various reasons for out of sequence progress, such as an error in the relationship of the original plan or the successor activity started early to provide steady work flow to equipment and crew. Out of sequence often occurs on construction projects, thus management methods must be developed to address the situation.⁴ Oracle Primavera P6™ has different schedule calculation options including retained logic, progress override and actual dates.

¹ AACE International Recommended Practice No. 10S-90, Cost Engineering Terminology

² AACE International Recommended Practice No. 24R-03, Developing Activity Logic

³ AACE International Recommended Practice No. 49R-06, Identifying the Critical Path

⁴ Scheduling Best Practices by Warner Consulting. Retrieved off world wide web at <http://warnercon.com/wp-content/uploads/2012/07/Article-9-Handling-Out-of-Sequence-Progress1.pdf>

These settings are shown below:

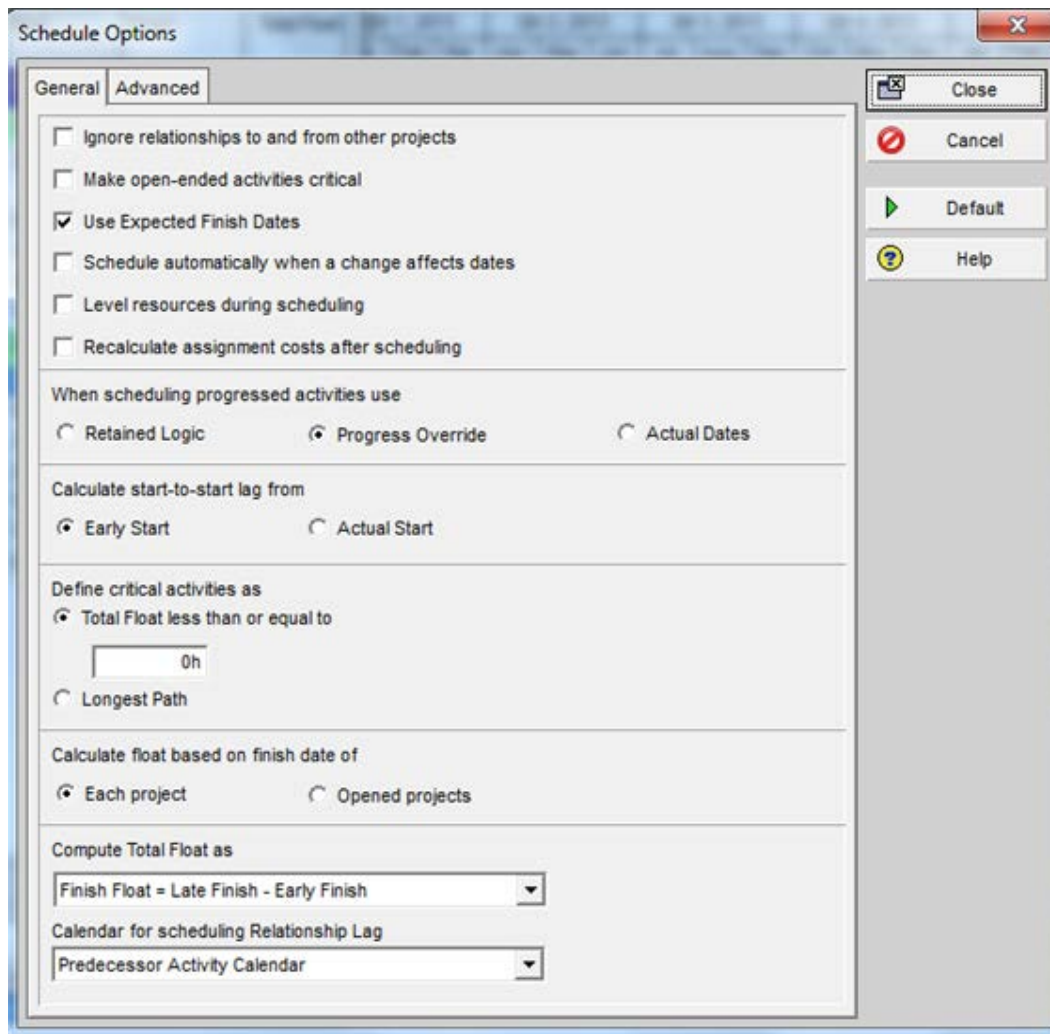
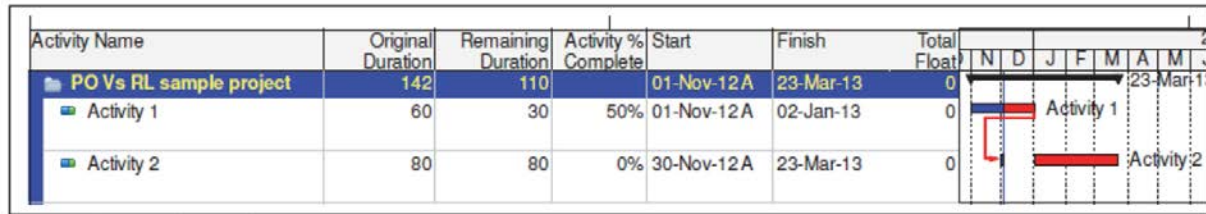


Figure 1 — Oracle Primavera P6™ Calculation settings⁵

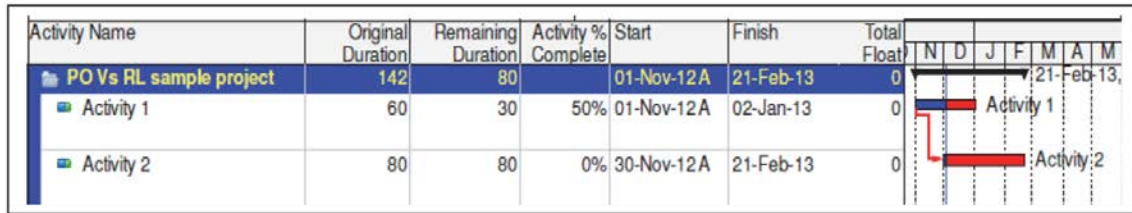
As long as schedule progress occurs as planned there is no difference in schedule calculation results comparing the three methods. When actual progress is not consistent with planned schedule logic, then out of sequence occurs and there could be notable difference between schedule calculation options. Methods are required for managing retained logic or progress override as these calculation options will cause differing results and could result in inaccurate dates and impact delay analysis of in progress schedules.

Analysis

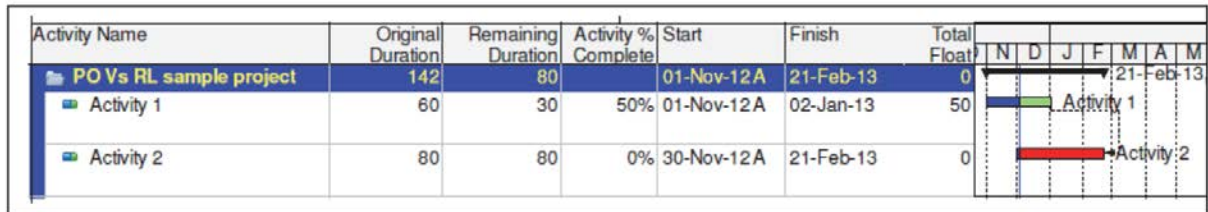
Retained Logic (RL): The retained logic schedule calculation setting holds schedule logic constant during calculation of in progress schedules. The calculation adds activity remaining duration in the forward pass calculation for early dates and waits until activity predecessors are completed. Below is a demonstration of RL option results with different logical relationships.



RL with FS Relationship



RL with SS Relationship

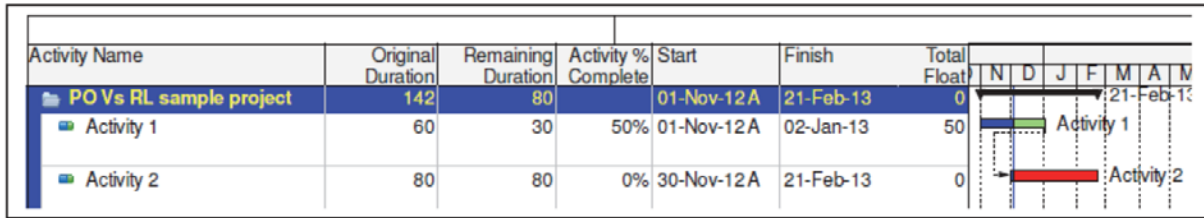


RL with FF Relationship

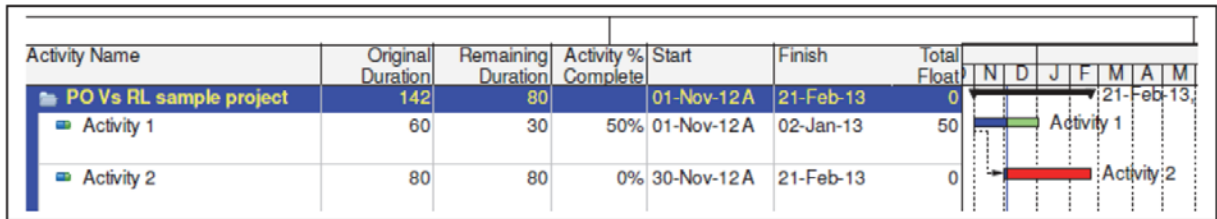
Figure 2 — Demonstration of Retained Logic Option Calculations

Figure 2 illustrates that when the retained logic calculation setting is utilized that it has impact on completion the date of activity 2 when using the FS relationship. For both SS and FF relationships there is no effect on the remaining early start and completion date of the Activity 2. The completion date of activity 2 is calculated based on its own remaining duration. However when utilizing a FF relationship, Activity 1 is rendered not critical and has total float equal to difference between early finish dates of Activity 2 and Activity 1.

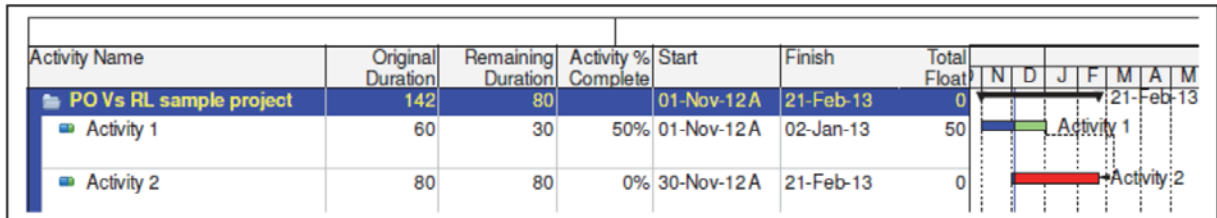
Progress Override (PO): When the progress override schedule calculation option setting is utilized, Oracle Primavera P6™ ignores the predecessor activity relationships and calculates the activity completion date based on its own remaining duration.



PO with FS logic



PO with SS logic

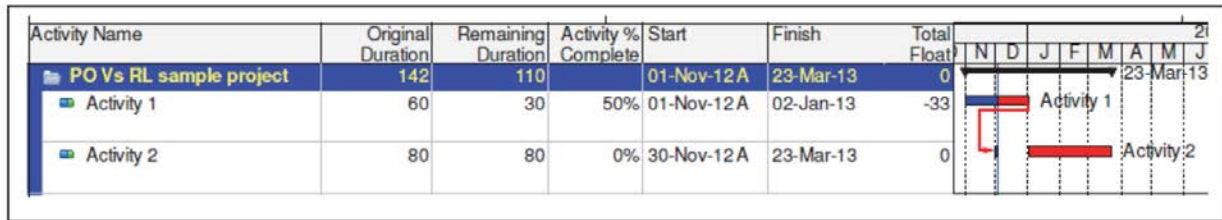


PO with FF logic

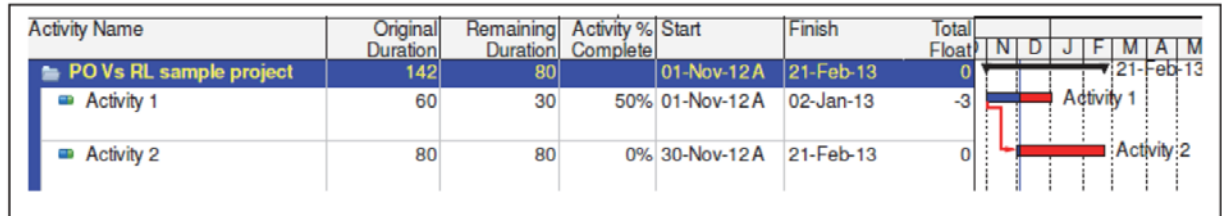
Figure 3 — Demonstration of Progress Override Schedule Calculations Option

Figure 3 displays when the progress override option is selected that all predecessor logic for the out of sequence activities is ignored for every relationship type and the remaining early start of Activity 2 is equal to the data date. Activity 1 and Activity 2 remaining start and finish dates are calculated solely by adding remaining duration to the data date. Also in all cases Activity 1 is no longer critical and has total float equal to the difference between early finish dates of Activity 2 and Activity 1.

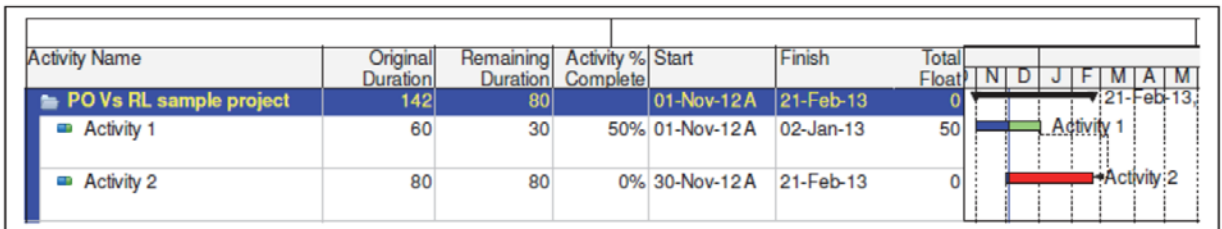
Actual Dates (AD): When the actual dates schedule calculation option setting is utilized, Oracle Primavera P6™ sets the late finish date of the predecessor activity one day before the actual start of the out of sequence activity. This normally creates negative float to predecessor activities.



AD with FS logic



AD with SS logic



AD with FF logic

Figure 4 — Demonstration of Actual Date Schedule Calculations Option

The above examples illustrate that the actual dates option is creating negative float for the predecessor activity in case of FS and SS relationships. However in the case of a FF relationship the finish date of the predecessor activity is 1 day before its successor activity. The actual date calculation on FF relationships removes the activity from the critical path and results in 50 days of total float. This schedule calculation setting is not widely used due to its tendency to create negative float in both FS and SS relationships.

Both retained logic and progress override schedule calculation settings are universally utilized across many industries. It is imperative to understand that each of these schedule calculation settings will yield a different result and most especially for out of sequence progress activities. In case of out of sequence progress AACE RP-49R-06 suggests the use of retained logic schedule calculation because progress override can create orphaned predecessor activities and disregard logical constraints. Further AACE RP-49R-06, recommends if the retained logic method produces poor or inaccurate results, then the schedule network logic must be corrected. In some cases it is not practical to make changes to schedule logic before every schedule update submission for various reasons including specification requirements requiring approval of changes, large number of activities, and insufficient resources.

Schedule Specifications

The scheduling specification defines schedule development and update mechanics. Typically they are prepared by the owner and distributed to the contractor. Industry practice in the west is for scheduling specifications to become contract documents. However in other areas of the world scheduling specifications are issued as a guideline to follow without any contractual implication.

In western contracts, schedule calculation settings are commonly specified for construction projects within the bid documents and can be found in the schedule specification. In this case it is common for owners to specify the use of retained logic and correction of logic for out of sequence progress. Review of schedule specifications in other areas of the world does not consistently reflect inclusion of schedule calculation requirements. When specifically mentioned these specifications favor retained logic over progress override. Los Angeles Metropolitan Transportation Authority (LA Metro)⁵ requires that schedule networks must use retained logic CPM precedence diagram methods of scheduling. When activities are worked out of sequence compared to the logic of the Baseline CPM Schedule, this logic will need to be revised as necessary in schedule update submissions. All logic revisions must be explained in the written narrative. Western owners also typically specify the requirements for approval of schedule changes whereby schedule logic changes must be pre-approved. LA Metro requires that any schedule changes, including changes to the logic sequence or activity durations and the impacts to the overall Contract must be explained and that the contractor must meet monthly prior to schedule submission for purposes of explaining changes to the schedule. Both of these clauses are quite commonly found in western owner schedule specifications and cause for concern of contractors.

Schedules that utilize the retained logic calculation when not managed properly will reflect erroneous completion dates especially if out of sequence progress is not corrected. In these cases the calculated early dates of successor activities following the out of sequence activities will have incorrect dates.

In Middle Eastern countries most of the schedule specifications do not cover handling out of sequence progress. Some specifications specify the use of retained logic but do not require logic correction of out of sequence activities. Kahramaa (Qatar General Electricity and Water Corporation)⁶ and Limitless (Dubai UAE) schedule requirements do not mention handling out-of-sequence activities. The Lusail Development schedule specification⁷ in Qatar does mention retained logic but again is silent with reference to out-of-sequence activities and any required logic corrections.

⁵ Standard Schedule Specification - 01 29 76-Cost Schedule Integration System, Los Angeles Metropolitan Transportation Authority

⁶ Kahramaa Planning and Progress Monitoring Guidelines – Phase 10 Projects

⁷ Lusail Development Projects Design-Build contracts Tender Appendix F, Administrative Procedures

Industry Examples

Example 1

RL Vs PO Impact on Schedule Updates: Below is an industry example of a schedule baseline and subsequent update comprising a few activities to demonstrate the different schedule calculation results based on the retained logic and progress override calculation settings.

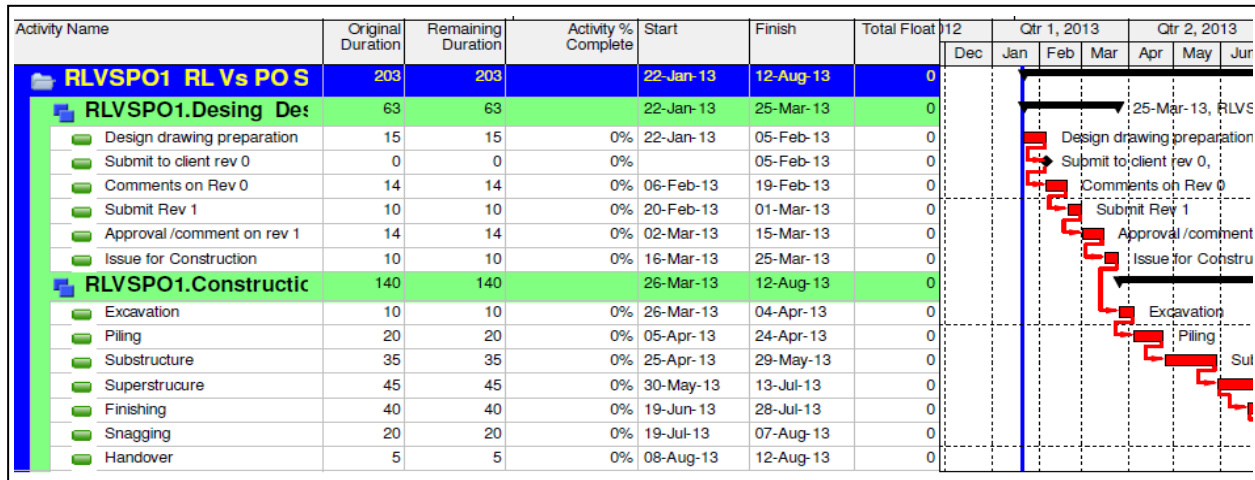


Figure 5 — Industry Example Sample Baseline

In Figure 5 the schedule fragnet comprises design and construction activities. According to baseline project start date is 22-Jan-13 and Completion date is 12-Aug-13. Schedule logic is applied where no construction activity will start unless construction drawings are issued

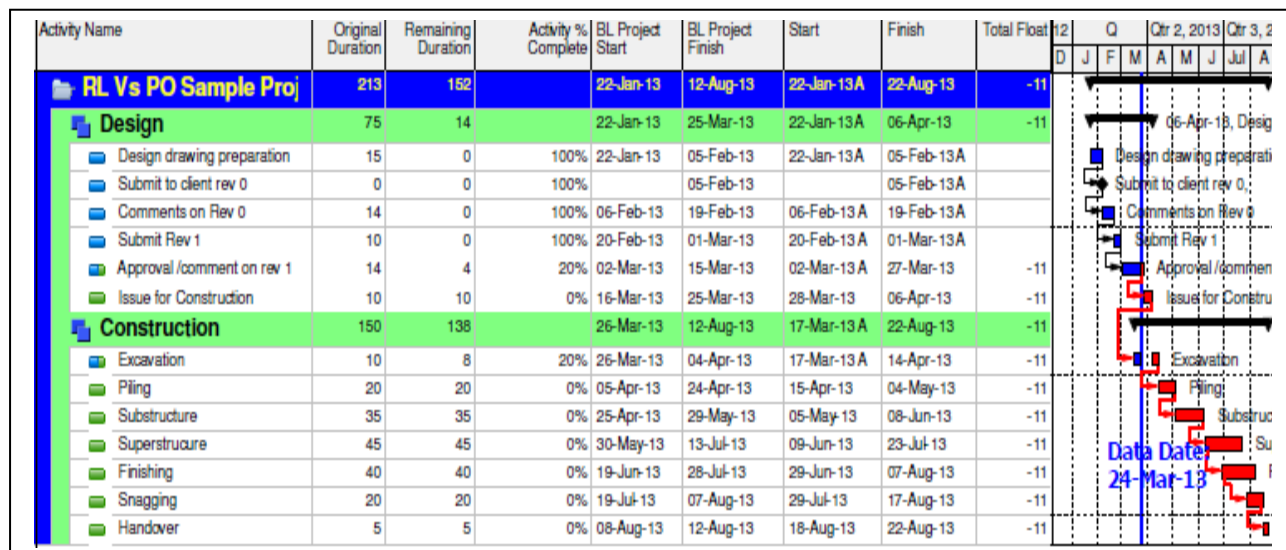


Figure 6 — Industry Example Schedule Update

Figure 6 reflects actual progress of the same schedule fragnet displaying that as of 24-Mar-13 the contractor received comments on rev0 drawings and submitted rev 1 as per schedule. However the in-progress activity is delayed. The contractor commenced excavation on 17-Mar-

13 out of sequence and ahead of schedule compared to the baseline. Schedule logic has not been revised to reflect the out of sequence progress and as a result reflects 11 days of delay.

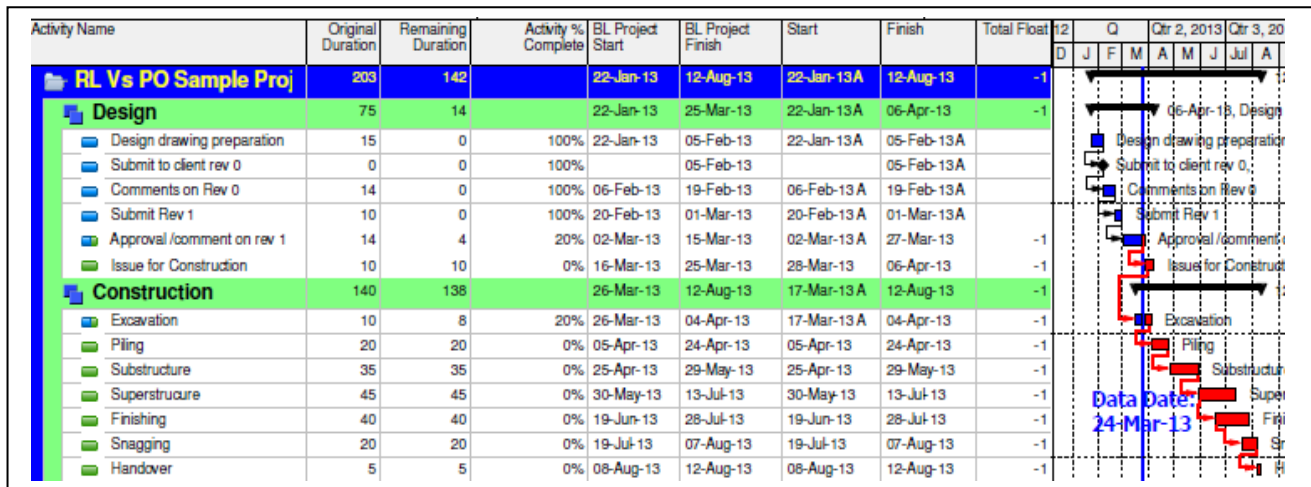


Figure 7 – Industry Example Reflecting Out of Sequence Progress Correction

Figure 7 depicts the change in schedule calculation when corrections are made to the out of sequence schedule progress activity prior to scheduling. Schedule logic corrections could include either changing the relationship type between drawing approval and excavation from FS to SS or removing the relationship in its entirety. In this case the remaining early start date of excavation activity will change to 28-Mar-13 and so the completion of this activity became 10 days prior but still showing negative float of -1. If excavation is delinked from the issue for construction activity, and linked to the approval/comment on rev 1 activity with a FS link then the same calculation result occurs and the remaining early start date will be the same (28-Mar-13).

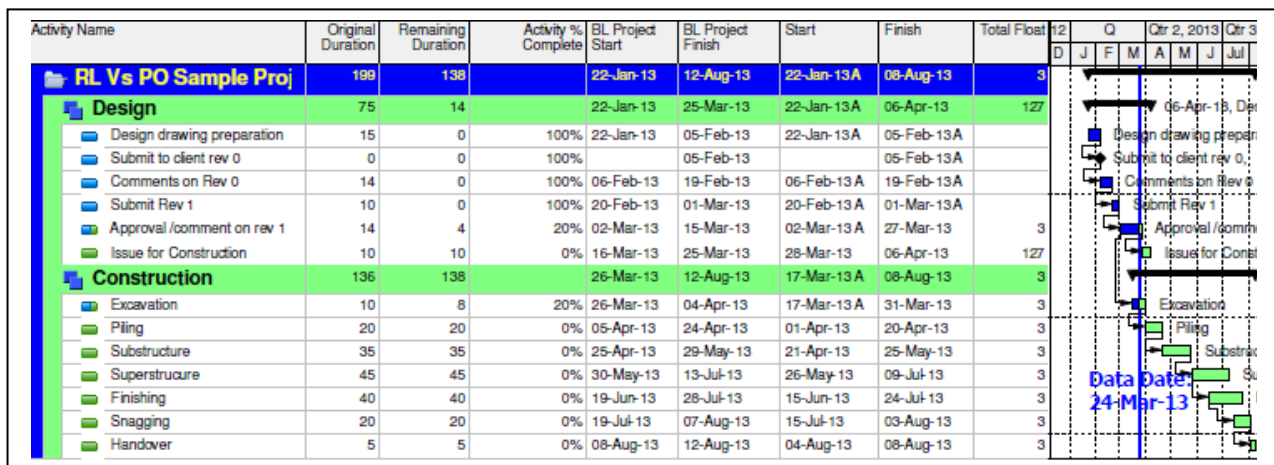


Figure 8 - Industry Example Reflecting Retained Logic and Correction of Out of Sequence Progress

Figure 8 illustrates the updated schedule activities with the excavation activity linked to the approval/comment on rev 1 activity with a SS relationship. In this case the schedule reflects mitigation efforts by the contractor and provides 3 days float to future activities.

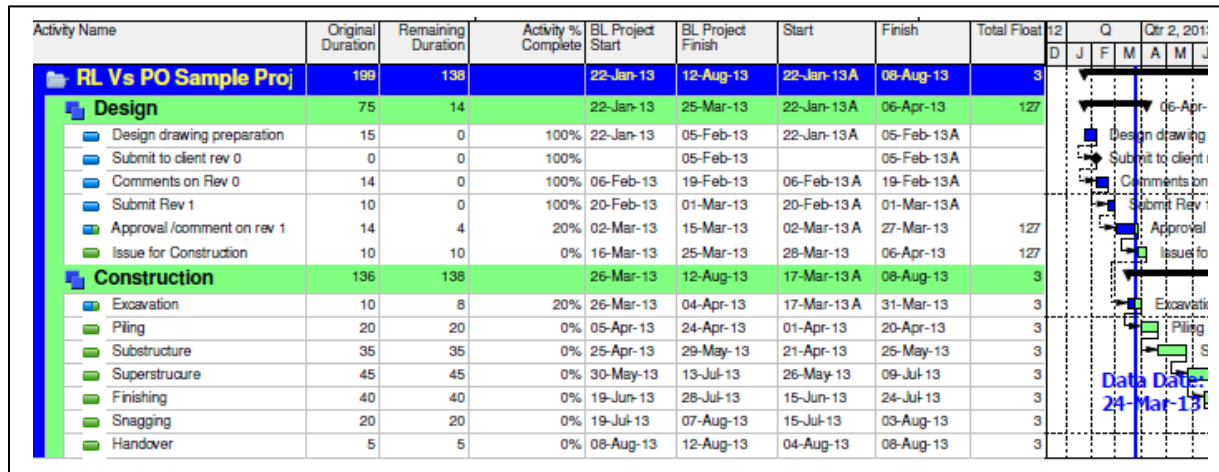


Figure 9 – Industry Example Reflecting Progress Override

Figure 9 reflects a schedule update as calculated with progress override and no schedule logic corrections. In this case the excavation activity which is occurring out of sequence ignores logic of its predecessor and is progressed as work is occurring on site. Amazingly the results are same as the results we got utilizing retained logic by linking excavation the approval/comment on rev 1 activity.

Example 2

The following examples are from a project in progress in Qatar regarding substation construction. In this case schedule specifications are silent on utilization of retained logic or progress override and the contractor is free to choose. Schedule specifications are stringent regarding submission of monthly updates and pre-approval of schedule logic changes. In order to obtain approval of schedule logic changes the contractor must follow a prescribed and lengthy process. The contractor is working on out of sequence in effort to complete the project earlier than planned.

Activities

Activities | Projects

Layout: Classic Schedule Layout

Filter: All Activities

Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float
11kV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A		
HV/EHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A		
Substation Control System	41	41	0%	05-Sep-12 A	17-Dec-12	07-Nov-12	19
Relay Control Panels	40	40	0%	09-Sep-12 A	24-Feb-13	16-Jan-13	-50
Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A		
LVAC & DCDB	5	0	100%	01-Jul-12 A	10-Jul-12 A		
External cables	5	5	0%	17-Nov-12	21-Nov-12	17-Nov-12	68
End to end Integrity test	3	3	0%	25-Feb-13	27-Feb-13	25-Feb-13	-50
NOSP A	0	0	0%	28-Feb-13	28-Feb-13	28-Feb-13	-50
DCC Testing	7	7	0%	28-Feb-13	06-Mar-13	28-Feb-13	-50
NCC Testing	6	6	0%	07-Mar-13	12-Mar-13	07-Mar-13	-50
Sign of all test reports	1	1	0%	13-Mar-13	13-Mar-13	13-Mar-13	-46
Telecom testing by others	1	1	0%	27-Nov-12	27-Nov-12	27-Nov-12	60
ERECTION COMPLETION CERTIFICATE	2	2	0%	14-Mar-13	15-Mar-13	14-Mar-13	-46
Submission of request for Erection completion certificate	1	1	0%	14-Mar-13	14-Mar-13	14-Mar-13	-46
Issuance of Erection completion certificate	1	1	0%	15-Mar-13	15-Mar-13	15-Mar-13	-46
NOTIFICATION OF SAFETY PRECAUTIONS	10	10	0%	13-Mar-13	22-Mar-13	13-Mar-13	-50
Overall Stability Test	1	1	0%	13-Mar-13	13-Mar-13	13-Mar-13	-50
NOSP-B Registration	2	2	0%	14-Mar-13	15-Mar-13	14-Mar-13	-50
NOSP-B Defect Clearance	2	2	0%	16-Mar-13	17-Mar-13	16-Mar-13	-50
Issuance of NOSP B	2	2	0%	18-Mar-13	19-Mar-13	18-Mar-13	-50
Readiness for Commissioning for energization	2	2	0%	20-Mar-13	21-Mar-13	20-Mar-13	-50
Connection to Network by KM(Energization)	1	1	0%	22-Mar-13	22-Mar-13	22-Mar-13	-50
RELIABILITY AND PERFORMANCE TEST	47	47	0%	23-Mar-13	08-May-13	23-Mar-13	-50
Certificate of Operation	1	1	0%	23-Mar-13	23-Mar-13	23-Mar-13	-50
Reliability Run	30	30	0%	24-Mar-13	22-Apr-13	24-Mar-13	-50
Defect Clearance	14	14	0%	23-Apr-13	06-May-13	23-Apr-13	-50
Taking Over Certificate	2	2	0%	07-May-13	08-May-13	07-May-13	-50
Contract Completion	0	0	0%		08-May-13		-50

General

Status

Resources

Predecessors

Successors

Relationships

Activity 10TS014

Relay Control Panels

Predecessors

Project ID	WBS	Activity ID	Activity Name	Relations	Lag	Activity Status	Primary Resource
06BC2-UP-6	06BC2-UP-6.BC	06EN600	AFC	FS	0	In Progress	
06BC2-UP-6	06BC2-UP-6.BC	06CB2390	Control Cabling 11kV Switchgear to 11kV	FS	0	Completed	
06BC2-UP-6	06BC2-UP-6.BC	06EQ2290	11kV PROTECTION SYSTEM for Switchgear	FS	0	Completed	

Successors

Project ID	WBS
06BC2-UP-6	06BC2-UP-6
06BC2-UP-6	06BC2-UP-6

Figure 10 — Industry Example 2, 25-Oct-12 Update with Retained Logic Calculation

Figure 10 displays the October 25, 2012 schedule update from the contractor utilizing retained logic without revision of out of sequence progress. This update reflects that the 'Relay Control Panels' activity is occurring out of sequence and is driving the project completion date. This activities predecessor is incomplete for activity 'AFC'. This schedule displays incorrect early date calculation for all downstream activities on the critical path to project completion and reflects critical path delay of -50 days. In an effort to mitigate the relay control panels activity has started early but mitigation efforts are not shown by calculated dates because out of sequence progress logic was not corrected.

Activities								
Activities Projects								
Layout: Classic Schedule Layout				Filter: All Activities				
Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float	
11kV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A			
HV/EHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A			
Substation Control System	41	41	0%	05-Sep-12 A	03-Dec-12	24-Oct-12	33	
Relay Control Panels	40	40	0%	09-Sep-12 A	02-Dec-12	24-Oct-12	34	
Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A			
LVAC & DCDB	5	0	100%	01-Jul-12 A	10-Jul-12 A			
External cables	5	5	0%	24-Oct-12	28-Oct-12	24-Oct-12	92	
End to end Integrity test	3	3	0%	04-Dec-12	06-Dec-12	04-Dec-12	33	
NOSPA	0	0	0%	07-Dec-12	07-Dec-12	07-Dec-12	33	
DCC Testing	7	7	0%	07-Dec-12	13-Dec-12	07-Dec-12	33	
NCC Testing	6	6	0%	14-Dec-12	19-Dec-12	14-Dec-12	33	
Sign of all test reports	1	1	0%	20-Dec-12	20-Dec-12	20-Dec-12	37	
Telecom testing by others	1	1	0%	13-Nov-12	13-Nov-12	13-Nov-12	74	
ERECTION COMPLETION CERTIFICATE	2	2	0%	21-Dec-12	22-Dec-12	21-Dec-12	37	
Submission of request for Erection compl	1	1	0%	21-Dec-12	21-Dec-12	21-Dec-12	37	
Issuance of Erection completion certificat	1	1	0%	22-Dec-12	22-Dec-12	22-Dec-12	37	
NOTIFICATION OF SAFETY PRECAUTIONS	10	10	0%	20-Dec-12	29-Dec-12	20-Dec-12	33	
Overall Stability Test	1	1	0%	20-Dec-12	20-Dec-12	20-Dec-12	33	
NOSP-B Registration	2	2	0%	21-Dec-12	22-Dec-12	21-Dec-12	33	
NOSP-B Defect Clearance	2	2	0%	23-Dec-12	24-Dec-12	23-Dec-12	33	
Issuance of NOSP B	2	2	0%	25-Dec-12	26-Dec-12	25-Dec-12	33	
Readiness for Commissioning for energis	2	2	0%	27-Dec-12	28-Dec-12	27-Dec-12	33	
Connection to Network by KM(Energizatio	1	1	0%	29-Dec-12	29-Dec-12	29-Dec-12	33	
RELIABILITY AND PERFORMANCE TEST	47	47	0%	30-Dec-12	14-Feb-13	30-Dec-12	33	
Certificate of Operation	1	1	0%	30-Dec-12	30-Dec-12	30-Dec-12	33	
Reliability Run	30	30	0%	31-Dec-12	29-Jan-13	31-Dec-12	33	
Defect Clearance	14	14	0%	30-Jan-13	12-Feb-13	30-Jan-13	33	
Taking Over Certificate	2	2	0%	13-Feb-13	14-Feb-13	13-Feb-13	33	
Contract Completion	0	0	0%		14-Feb-13		33	

General	Status	Resources	Predecessors	Successors	Relationships
Activity: 10TS014 Relay Control Panels					
Predecessors					
Project ID	WBS	Activity ID	Activity Name	Relations	Lag
068C2-UP-6	068C2-UP-6.BC	06EN600	AFC	FS	0
068C2-UP-6	068C2-UP-6.BC	08CB2390	Control Cabling 11kV Switchgear to 11kV	FS	0
068C2-UP-6	068C2-UP-6.BC	08EI2290	11kV PROTECTION SYSTEM for Switchgear	FS	0
Successors					
Project ID	WBS	Activity ID	Activity Name	Relations	Lag
068C2-UP-6	068C2-UP-6.BC	06EN600	AFC	FS	0
068C2-UP-6	068C2-UP-6.BC	08CB2390	Control Cabling 11kV Switchgear to 11kV	FS	0
068C2-UP-6	068C2-UP-6.BC	08EI2290	11kV PROTECTION SYSTEM for Switchgear	FS	0

Figure 11 — Industry Example 2, 25-Oct-12 Update with Progress Override Calculation

Figure 11 displays the October 25, 2012 schedule update utilizing progress override calculation. The 'Relay and Control Panel' activity which has started out of sequence has its remaining early dates calculated by ignoring incomplete predecessor logic. The schedule mitigation results are shown and the contract completion reflects early completion by 33 days.

Activities

Activities | Projects

Layout: Classic Schedule Layout

Filter: All Activities

Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float
 Station Transformer	5	0	100%	06-Aug-12 A	14-Aug-12 A		
 11kV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A		
 HV/EHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A		
 Substation Control System	41	0	100%	05-Sep-12 A	22-Nov-12 A		
 Relay Control Panels	40	0	100%	09-Sep-12 A	22-Nov-12 A		
 Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A		
 LVAC & DCDB	5	0	100%	01-Jul-12 A	10-Jul-12 A		
 External cables	5	0	100%	01-Nov-12 A	23-Nov-12 A		
 End to end Integrity test	3	0	100%	22-Nov-12 A	23-Nov-12 A		
 NOSPA	0	0	100%	01-Nov-12 A	10-Nov-12 A		
 DCC Testing	7	0	100%	22-Nov-12 A	23-Nov-12 A		
 NCC Testing	6	0	100%	23-Nov-12 A	24-Nov-12 A		
 Sign of all test reports	1	0	100%	24-Nov-12 A	24-Nov-12 A		
 Telecom testing by others	1	0	100%	01-Nov-12 A	10-Nov-12 A		
 ERECTION COMPLETION CERTIFICATE	16	0		20-Nov-12 A	23-Nov-12 A		
 Submission of request for Erection compl	1	0	100%	20-Nov-12 A	22-Nov-12 A		
 Issuance of Erection completion certificat	1	0	100%	23-Nov-12 A	23-Nov-12 A		
 NOTIFICATION OF SAFETY PRECAUTIONS	67	0		20-Nov-12 A	24-Nov-12 A		
 Overall Stability Test	1	0	100%	20-Nov-12 A	22-Nov-12 A		
 NOSP-B Registration	2	0	100%	23-Nov-12 A	23-Nov-12 A		
 NOSP-B Defect Clearance	2	0	100%	23-Nov-12 A	23-Nov-12 A		
 Issuance of NOSP B	2	0	100%	23-Nov-12 A	23-Nov-12 A		
 Readiness for Commissioning for energis	2	0	100%	24-Nov-12 A	24-Nov-12 A		
 Connection to Network by KM(Energizatic	1	0	100%	24-Nov-12 A	24-Nov-12 A		
 RELIABILITY AND PERFORMANCE TEST	47	47		04-Feb-13	22-Mar-13	04-Feb-13	-3
 Certificate of Operation	1	1	0%	04-Feb-13	04-Feb-13	04-Feb-13	-3
 Reliability Run	30	30	0%	05-Feb-13	06-Mar-13	05-Feb-13	-3
 Defect Clearance	14	14	0%	07-Mar-13	20-Mar-13	07-Mar-13	-3
 Taking Over Certificate	2	2	0%	21-Mar-13	22-Mar-13	21-Mar-13	-3
 Contract Completion	0	0	0%		22-Mar-13		-3

General

Status

Resources

Predecessors

Successors

Relationships

Activity 10TS014

Relay Control Panels

Predecessors

Project ID	WBS	Activity ID	Activity Name	Relations	Lag	Activity Status
	06BC2-UP-7	06BC2-UP-7.BC	06EN600	AFC	FS	0 In Progress
	06BC2-UP-7	06BC2-UP-7.BC	08CB2390	Control Cabling 11kV Switchgear to 11kV	FS	0 Completed
	06BC2-UP-7	06BC2-UP-7.BC	08EI2290	11kV PROTECTION SYSTEM for Switchg	FS	0 Completed

Successors

Project ID	WBS	Activity ID	Activity Name	Relations	Lag	Activity Status
	06BC2-UP-7	06BC2-UP-7.BC	10TS030			
	06BC2-UP-7	06BC2-UP-7.BC	10TS034			

Figure 12 — Industry Example 2, 25-Nov-12 Update with Retained Logic Calculation

Figure 12 displays the November 25, 2012 update utilizing retained logic calculation. The 'relay control panels' activity and many of its successors that were driving the project completion date are completed, however the 'AFC' predecessor is still in progress and delaying the project completion by 3 days.

Activities

Activities | Projects

Layout: Classic Schedule Layout

Filter: All Activities

Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float
Station Transformer	5	0	100%	06-Aug-12 A	14-Aug-12 A		
11kV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A		
HV/EHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A		
Substation Control System	41	0	100%	05-Sep-12 A	22-Nov-12 A		
Relay Control Panels	40	0	100%	09-Sep-12 A	22-Nov-12 A		
Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A		
LVAC & DCDB	5	0	100%	01-Jul-12 A	10-Jul-12 A		
External cables	5	0	100%	01-Nov-12 A	23-Nov-12 A		
End to end Integrity test	3	0	100%	22-Nov-12 A	23-Nov-12 A		
NOSP A	0	0	100%	01-Nov-12 A	10-Nov-12 A		
DCC Testing	7	0	100%	22-Nov-12 A	23-Nov-12 A		
NCC Testing	6	0	100%	23-Nov-12 A	24-Nov-12 A		
Sign of all test reports	1	0	100%	24-Nov-12 A	24-Nov-12 A		
Telecom testing by others	1	0	100%	01-Nov-12 A	10-Nov-12 A		
ERECTION COMPLETION CERTIFICATE	16	0	100%	20-Nov-12 A	23-Nov-12 A		
Submission of request for Erection compl	1	0	100%	20-Nov-12 A	22-Nov-12 A		
Issuance of Erection completion certificat	1	0	100%	23-Nov-12 A	23-Nov-12 A		
NOTIFICATION OF SAFETY PRECAUTIONS	67	0	100%	20-Nov-12 A	24-Nov-12 A		
Overall Stability Test	1	0	100%	20-Nov-12 A	22-Nov-12 A		
NOSP-B Registration	2	0	100%	23-Nov-12 A	23-Nov-12 A		
NOSP-B Defect Clearance	2	0	100%	23-Nov-12 A	23-Nov-12 A		
Issuance of NOSP B	2	0	100%	23-Nov-12 A	23-Nov-12 A		
Readiness for Commissioning for energis	2	0	100%	24-Nov-12 A	24-Nov-12 A		
Connection to Network by KM(Energizatio	1	0	100%	24-Nov-12 A	24-Nov-12 A		
RELIABILITY AND PERFORMANCE TEST	47	47	0%	24-Nov-12	09-Jan-13	24-Nov-12	69
Certificate of Operation	1	1	0%	24-Nov-12	24-Nov-12	24-Nov-12	69
Reliability Run	30	30	0%	25-Nov-12	24-Dec-12	25-Nov-12	69
Defect Clearance	14	14	0%	25-Dec-12	07-Jan-13	25-Dec-12	69
Taking Over Certificate	2	2	0%	08-Jan-13	09-Jan-13	08-Jan-13	69
Contract Completion	0	0	0%		09-Jan-13		69

General | Status | Resources | Predecessors | Successors | Relationships

Activity 10TS014 Relay Control Panels

Predecessors

Project ID	WBS	Activity ID	Activity Name	Relations	Lag	Activity Status
06BC2-UP-7	06BC2-UP-7.BC	06EN600	AFC	FS	0	In Progress
06BC2-UP-7	06BC2-UP-7.BC	08CB2390	Control Cabling 11kV Switchgear to 11kV	FS	0	Completed
06BC2-UP-7	06BC2-UP-7.BC	08EQ2290	11kV PROTECTION SYSTEM for Switchgear	FS	0	Completed

Successors

Project ID	WBS	Activity ID	Activity Name	Relations	Lag	Activity Status
06BC2-UP-7	06BC2-UP-7.BC	10TS030				End
06BC2-UP-7	06BC2-UP-7.BC	10TS034				DCC

Figure 13 — Industry Example 2, 25-Nov-12 Update with Progress Override Calculation

Figure 13 displays the November 25, 2012 update utilizing progress override calculation. Incomplete predecessor logic is ignored and the completion date is calculated based upon the remaining duration of in progress activities and their successors. Contract completion reflects an early completion of 69 days.

Activities							
Activities Projects							
Layout: Classic Schedule Layout				Filter: All Activities			
Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float
Station Transformer	5	0	100%	06-Aug-12 A	14-Aug-12 A		
11KV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A		
HV/EHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A		
Substation Control System	41	0	100%	05-Sep-12 A	22-Nov-12 A		
Relay Control Panels	40	0	100%	09-Sep-12 A	22-Nov-12 A		
Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A		
LVAC & DCDB	5	0	100%	01-Jul-12 A	10-Jul-12 A		
External cables	5	0	100%	01-Nov-12 A	23-Nov-12 A		
End to end Integrity test	3	0	100%	22-Nov-12 A	23-Nov-12 A		
NOSPA	0	0	100%	01-Nov-12 A	10-Nov-12 A		
DCC Testing	7	0	100%	22-Nov-12 A	23-Nov-12 A		
NCC Testing	6	0	100%	23-Nov-12 A	24-Nov-12 A		
Sign of all test reports	1	0	100%	24-Nov-12 A	24-Nov-12 A		
Telecom testing by others	1	0	100%	01-Nov-12 A	10-Nov-12 A		
ERECTION COMPLETION CERTIFICATE	16	0		20-Nov-12 A	23-Nov-12 A		
Submission of request for Erection compl	1	0	100%	20-Nov-12 A	22-Nov-12 A		
Issuance of Erection completion certificat	1	0	100%	23-Nov-12 A	23-Nov-12 A		
NOTIFICATION OF SAFETY PRECAUTIONS	67	0		20-Nov-12 A	24-Nov-12 A		
Overall Stability Test	1	0	100%	20-Nov-12 A	22-Nov-12 A		
NOSP-B Registration	2	0	100%	23-Nov-12 A	23-Nov-12 A		
NOSP-B Defect Clearance	2	0	100%	23-Nov-12 A	23-Nov-12 A		
Issuance of NOSP B	2	0	100%	23-Nov-12 A	23-Nov-12 A		
Readiness for Commissioning for energis	2	0	100%	24-Nov-12 A	24-Nov-12 A		
Connection to Network by KM(Energizatic	1	0	100%	24-Nov-12 A	24-Nov-12 A		
RELIABILITY AND PERFORMANCE TEST	31	16		25-Nov-12 A	18-Jan-13	03-Jan-13	60
Certificate of Operation	1	0	100%	25-Nov-12 A	25-Nov-12 A		
Reliability Run	30	0	100%	26-Nov-12 A	25-Dec-12 A		
Defect Clearance	14	14	0%	03-Jan-13	16-Jan-13	03-Jan-13	60
Taking Over Certificate	2	2	0%	17-Jan-13	18-Jan-13	17-Jan-13	60
Contract Completion	0	0	0%		18-Jan-13		60

General	Status	Resources	Predecessors	Successors	Relationships
Activity 00MS000 Contract Effective date					
Predecessors			Successors		
Project ID	WBS	Activity ID	Activity Name	Relations	Lag
			Activity Status		
			Project ID WBS Activity ID Activity Name Relations Lag Activity Status		
			06BC2-UP-4 06BC2-UP-8.BK 00MS002 K		

Figure 14 — Industry Example 2, 25-Dec-12 Update with Retained Logic Calculation

Figure 14 reflects the December 15, 2012 with retained logic calculation. Most activities are completed so out of sequence progress is not a factor. The schedule that was showing delay the prior month now reflects early completion by 60 days.

Activities								
Activities Projects								
Layout: Classic Schedule Layout			Filter: All Activities					
Activity Name	Original Duration	Remaining Duration	Activity %	Start	Finish	Remaining Early Start	Total Float	
Station Transformer	5	0	100%	06-Aug-12 A	14-Aug-12 A			
11kV NER	5	0	100%	23-Sep-12 A	24-Sep-12 A			
HV/FHV cable testing	4	0	100%	19-Sep-12 A	20-Sep-12 A			
Substation Control System	41	0	100%	05-Sep-12 A	22-Nov-12 A			
Relay Control Panels	40	0	100%	09-Sep-12 A	22-Nov-12 A			
Battery & Battery Charger and UPS	10	0	100%	09-Jul-12 A	31-Aug-12 A			
LVAC & DDCB	5	0	100%	01-Jul-12 A	10-Jul-12 A			
External cables	5	0	100%	01-Nov-12 A	23-Nov-12 A			
End to end integrity test	3	0	100%	22-Nov-12 A	23-Nov-12 A			
NOSP-A	0	0	100%	01-Nov-12 A	10-Nov-12 A			
DOC Testing	7	0	100%	22-Nov-12 A	23-Nov-12 A			
NOC Testing	6	0	100%	23-Nov-12 A	24-Nov-12 A			
Sign off all test reports	1	0	100%	24-Nov-12 A	24-Nov-12 A			
Telecom testing by others	1	0	100%	01-Nov-12 A	10-Nov-12 A			
ERECTION COMPLETION CERTIFICATE	16	0	100%	20-Nov-12 A	23-Nov-12 A			
Submission of request for Erection completion certificate	1	0	100%	20-Nov-12 A	22-Nov-12 A			
Issuance of Erection completion certificate	1	0	100%	23-Nov-12 A	23-Nov-12 A			
NOTIFICATION OF SAFETY PRECAUTIONS	67	0	100%	20-Nov-12 A	24-Nov-12 A			
Overall Stability Test	1	0	100%	20-Nov-12 A	22-Nov-12 A			
NOSP-E Registration	2	0	100%	23-Nov-12 A	23-Nov-12 A			
NOSP-E Defect Clearance	2	0	100%	23-Nov-12 A	23-Nov-12 A			
Issuance of NCSPE	2	0	100%	23-Nov-12 A	23-Nov-12 A			
Readiness for Commissioning for energization	2	0	100%	24-Nov-12 A	24-Nov-12 A			
Connection to Network by KM(Energization)	1	0	100%	24-Nov-12 A	24-Nov-12 A			
RELIABILITY AND PERFORMANCE TEST	31	16	0%	25-Nov-12 A	19-Jan-13	25-Dec-12	69	
Certificate of Operation	1	0	100%	25-Nov-12 A	25-Nov-12 A			
Reliability Run	30	0	100%	26-Nov-12 A	25-Dec-12 A			
Defect Clearance	14	14	0%	25-Dec-12	17-Jan-13	25-Dec-12		69
Taking Over Certificate	2	2	0%	08-Jan-13	09-Jan-13	08-Jan-13		69
Contract Completion	0	0	0%		09-Jan-13			69

General	Status	Resources	Predecessors	Successors	Relationships
Activity: JUMSU01 Contract Effective date:					
Predecessors					
Project ID	WRS	Activity ID	Activity Name	Relations	Lag
Successors					
Project ID	WRS	Activity ID	Activity Name	Relations	Lag
06BC2-LP4	06BC2-UP-8 B	06MS002	Click		

Figure 15 — Industry Example 2, 25-Dec-12 Update with Progress Override Calculation

Figure 15 displays the December 15, 2012 update utilizing progress override calculation. Again incomplete predecessor logic is ignored and the completion date is calculated based upon the remaining duration of in progress activities and their successors. Contract completion reflects an early completion of 69 days.

All of these examples illustrate the vast difference in calculated dates that are possible when using retained logic versus progress override schedule calculation most especially when out of sequence progress is not corrected. When utilizing the retained logic calculation there were false delays reflected for two updates and then suddenly reversed when nearing project completion. In each example where progress override calculation was utilized a projected early finish for the project was reflected.

Pros and Cons of Using Retained Logic vs Progress Override

Pros	Cons
Retained Logic is the most preferred method because logical relationships of activities are respected. When out of sequence logic is corrected the retained logic calculation option gives the most accurate results than any other option.	Retained logic reflects an inaccurate display of activity early dates where out of sequence activity logic is not corrected. This may also result in changing the project critical path based upon incorrect calculation. The out of sequence progress on the critical path could also reflect an inaccurate delayed project completion. From the contractor's point of view too much time is required to identify and address out of sequence activities and especially for large schedule networks. Correcting schedule logic becomes tedious and may not be limited to out of sequence activities. Further, schedule results may not be enhanced appreciably especially when compared against providing all scheduling resources necessary to correct the out of sequence logic.
Progress override allows out of sequence progress by ignoring predecessor relationship logic. Contractor's support that the same schedule calculation results are gained utilizing progress override without expending unnecessary resources to correct out of sequence logic. It is an easier approach with minimal extra efforts required to deal with out of sequence activities.	Progress override disregards the activity original logic. It also adds total float to the predecessor activities by ignoring the logic with out of sequence activities. In cases of critical path activities progress override will increase float available and offset delays.

Table 1 — Retained Logic Versus Progress Override**RL vs PO Impact on Claim Analysis:**

Understanding implications of retained logic versus progress override is very important regarding delay analysis. The following discusses forensic scheduling options as outlined in AACE RP 29R-03.

A) Planned Vs Actual or Plan Vs As Built:

This is the simplest method of delay analysis in which the actual progress is compared with planned and the difference considered. Often this is a post mortem analysis prepared after delay events to identify the difference between planned and actual. Normally the use of either retained logic or progress override calculation options will not impact delay evaluation since primarily the delay is analyzed based upon the difference between planned duration and actual duration of either an individual activity or project.

B) Impacted Baseline or Impacted As Planned:

In this delay analysis method the delay event is inserted in the as planned schedule to identify its impact on the overall completion date. This delay analysis method may establish good results especially when there is not much deviation between planned and actual progress. However, when actual progress differs from as planned and activities occur out of sequence then this method will likely produce inaccurate results. This delay analysis method is also not adversely impacted by retained logic or progress override as it often is a post mortem analysis reviewing the baseline without impact of out of sequence progress.

C) Collapsed As-Built or As-Built Less Delay:

This delay analysis method utilizes the as-built schedule for evaluation of delays. Delay events are subtracted from the schedule to review impact on progress of work. This method considers how original logic changed compared to baseline logic⁸. When developing the as built schedule it is very important to correct out of sequence logic to obtain accurate results.

D) Window Analysis or Contemporaneous Period Analysis:

This delay analysis method uses the project schedule updates to quantify the loss or gain of time along logic paths and identify activities responsible for critical path impacts. Although this method is retrospective, it relies on the forward-looking calculations made at the time the updates were prepared and to the right of the data date. Schedule updates are taken at an interval based on the frequency of updates on the project and as data are available. This technique does not involve the insertion or deletion of delays but instead is based on observing the behavior of the schedule network from update to update and measuring schedule variances based on unaltered, existing schedule logic.

This method relies on comparing the critical path of one update to a subsequent update. In this case correction of out of sequence logic for retained logic calculations is required to obtain accurate results. The AACE recommended practice does allow minor changes to the contemporaneous schedule and modifying logic for out of sequence activities.

E) Time Impact Analysis

The TIA is a 'forward-looking,' prospective schedule analysis technique that adds a modeled delay to an accepted contract schedule to determine the possible impact of that delay to

⁸ AACE recommended Practice 29R-03, Forensic Schedule Analysis

project completion⁹. In this method of analysis, a fragnet of delay events is created and inserted in the schedule update that is closest to the delay event. After adding the fragnet of delay events into the schedule, the schedule is analyzed to determine the difference between the un-impacted schedule and impacted schedule. Any difference is established as the effective delay.

Retained logic calculation and correction of out of sequence progress logic is required to analyze the pure delay between the two schedules. AACE recommended practice 52R-06 discusses the importance of out of sequence schedule logic correction and the effect of retained logic on CPM calculations. Progress override will ignore predecessor schedule logic and introduce additional float into the calculated schedule. It is imperative to correct out of sequence schedule logic to obtain true date calculation when using retained logic to analyze delays throughout the progress of the project.

Recommendation

As established in several examples out-of-sequence progress is a common occurrence and schedule quality control procedures and specifications are needed to address the matter. When schedule specification language does not exist problems pursue and differing stakeholders posture to benefit. The industry examples provided illustrated that when out of sequence work is not managed the scheduling calculation results do not reflect actual progress and logic corrections are required to accurately reflect the true picture. Where out of sequence schedule logic corrections are not made then progress override may be utilized to obtain better calculated date results. However, scheduling industry best practice recommends use of retained logic and correction out of sequence progress.

For delay analysis purposes it does not matter what calculation options are chosen when using the planned versus actual and impacted baseline or collapsed as built analysis methods. However, when performing contemporaneous delay analysis using either the windows technique or time impact analysis the retained logic calculation and out of sequence progress logic correction is required.

Conclusion

Schedule calculation options for both retained logic and progress override are important and when not managed properly can result in inaccurate results for in progress schedules. Scheduling best practice requires the utilization of both the retained logic calculation and correction of out of sequence progress schedule logic.

⁹ AACE Recommended Practice 52R-06, Time Impact Analysis – As applied in construction.

Schedule specification language is required to identify the schedule calculation method and correction of out of sequence progress logic. It is important to understand the impact of retained logic or progress override calculation options when performing delay analysis contemporaneously when using the windows or time impact analysis methods.

Contractors may fail to correct out of sequence schedule logic due to personnel skill sets, availability of resources, and contract clauses requiring protracted schedule logic approval processes. Where out of sequence progress schedule logic corrections are not made then progress override calculation is advised to obtain more realistic calculated date results.

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